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APPLICATION NO.	FILING DATE	FIRST NAMED INVEN		NTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/698,633	10/31/2003		Craig T. Sallin	8	TI-33791	3218
THE OF INVENTION: E	EFFICIENT ESD PROTECTI	ION WITH APPLI	CATION FOR LOW	V CAPACITANCE I/O I	PADS	
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Honeycutt Gary C. Typed or printed name Registration No. This collection of information is required by 37 CFR 1.311. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to proc an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 12 minutes to complete, including gathering, preparing, submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to comp this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P Box 1450, Alexandria, Virginia 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 14

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b. Applicant is no longer claiming SMALL ENTITY status. See 37 CFR 1.27(g)(2).

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a. Applicant claims SMALL ENTITY status. See 37 CFR 1.27.

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

re the Application of

**Applicant** 

Craig T. Salling, et al.

Docket Number: TI-33791

Serial No.: 10/698,633

Art Unit: 2826

Filed: 10/31/03

Examiner: Fazli Erdem

For:

Efficient ESD Protection with Application for Low

Capacitance I/O Pads

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NAME OF INVENTOR(S):  Craig T. Salling, et al.  TITLE OF INVENTION: Efficient ESD Protection with Application for Low Capacitance I/O Pads		Serial No.: 10/698,633 Filing Date: 10/31/03
TIFILE NO.: TI-33791	DEPOSIT ACCT. NO.: 20-0668	
FAXED: 11-11-04 DUE: 2-4-05 ATTY/SECY: GCH/kjv		

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